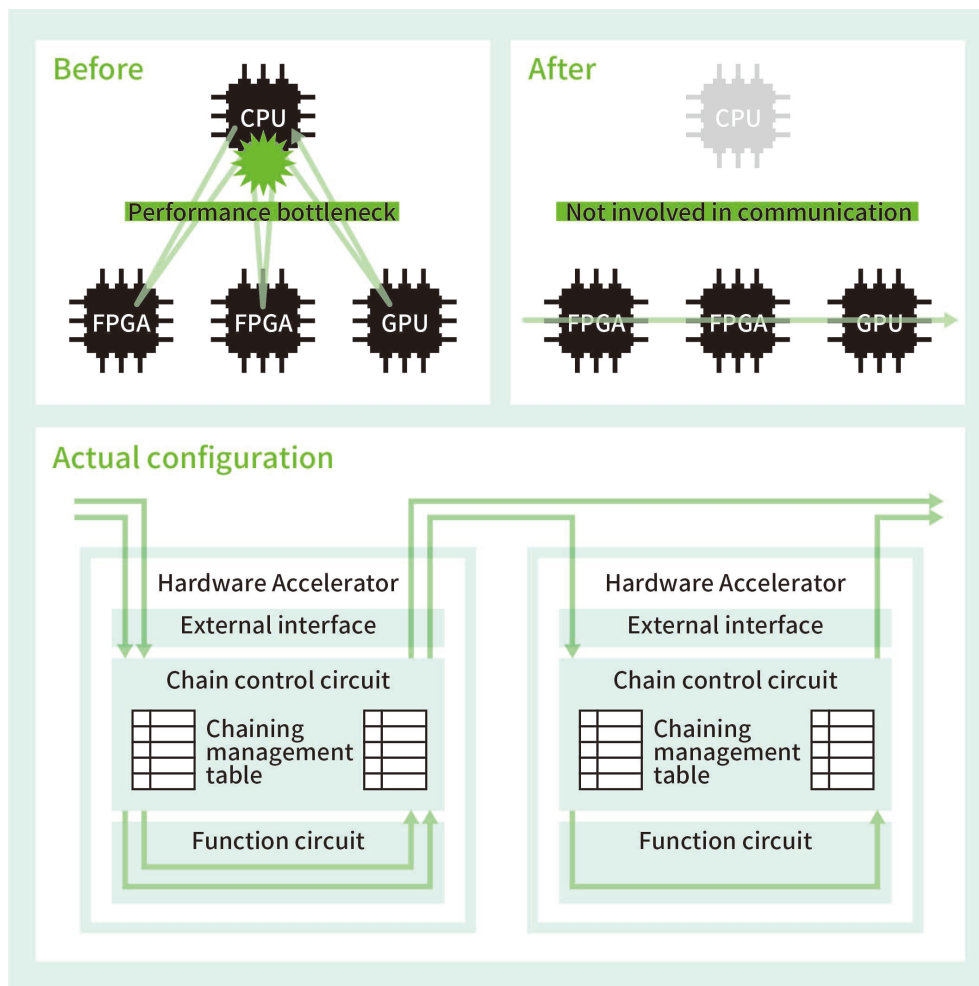


Direct data transfer control technology
between hardware accelerators
Efficient data processing infrastructure using
multiple hardware accelerators
#Customer Experience Value Creation



///Technical Issue

When the data processed by a high-speed HW accelerator system increases, data transfer control by the CPU becomes a bottleneck.

///Research Goal

Aiming at 1/2 lower power cost than conventional system by eliminating the CPU processing that controls the entire system.

---Technology

Complete data transmission autonomous control by hardware accelerator.

---Novelty

By controlling data transmission control between hardware accelerators outside of the data processing program, which was performed within the data processing program in the commercial technology, the CPU offload of data transfer control is further promoted, and the performance and power efficiency of the system are improved.

---Applicable Business

In the information and communications industry and service industry, this technology will be used to process large amounts of data in a short time with low latency, and to improve the efficiency of data collection, real-time processing, and speed up data analysis, as well as data formatting for AI processing (Technology establishment target: FY2028)