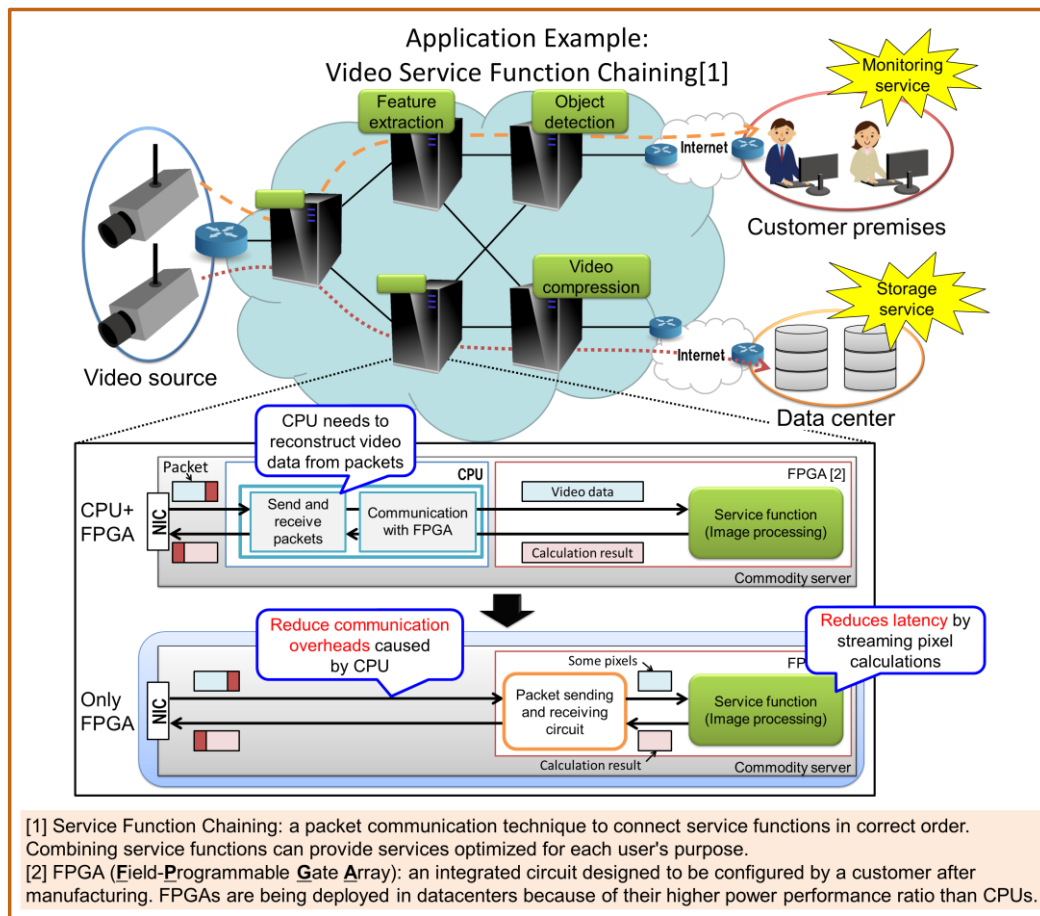


Realization of high-performance video processing on cloud platform utilizing FPGA



The current CPU+FPGA architecture has a large communication overhead between the CPU and FPGA. This issue currently prevents FPGAs from being used in some cloud services. Our FPGA accelerator solves the problem and enables low-latency and high-throughput cloud video services by streaming pixel-related calculations encapsulated in packets.